

## Flow-based Network Measurement— NetFlow & IPFIX

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### Abstract

This article describes our activities related to NetFlow, which is a de facto standard protocol for exporting flow information, and IPFIX, which is a protocol standardized in IETF (Internet Engineering Task Force). The method of exporting flow information by sending aggregated packets from routers, which has been deployed recently, is a promising alternative to the conventional method that obtains values of counters at interfaces in routers.

### 1. Introduction

To manage a network, it is necessary to monitor the amount of traffic and detect problems when they occur such as failures or congestion. The method that obtains the values of transmission and reception counters from interfaces in routers by SNMP (simple network management protocol) is widely used. Although SNMP is simple and lightweight to process, it does not let us easily analyze each connection. When detailed analysis is required, an alternative approach is to collect information about each packet. There are two methods. In one, an external collecting device collects copied packets by using port mirroring on switches. In the other, an external collecting device collects partial information about packets by using the sFlow protocol, which was invented by InMon Corporation.

Recently, a different method has been used. In this method, an external collecting device collects flow information, namely information about aggregated packets having the same attributes, which is classified in network equipment. This method enables the external collecting device to collect more detailed information than one using SNMP. On the other hand, less flow information is obtained than when raw packets are collected. Therefore, the method using flow infor-

mation is suitable when we want to know the rough tendency of network usage. Cisco's NetFlow is a de facto standard protocol for this method (**Table 1**). NetFlow technology can be classified into several versions: NetFlow version 9 (v9) has been published as RFC3954 (informational document).

IETF has also standardized the IPFIX (IP flow information export) protocol [1], [2], which is a standard protocol for IP (Internet protocol) networks based on NetFlow v9. IPFIX is a more reliable protocol than NetFlow v9, and it defines more collectable information than NetFlow v9.

### 2. Protocols for exporting flow information

In NetFlow and IPFIX, network equipment (e.g., a router) called an Exporter periodically sends flow information to a collecting device called a Collector (**Fig. 1**). Exporters export two kinds of information: Data and a Template. Data represents flow information. Its structure can be defined by the Templates in NetFlow v9 and in IPFIX because required traffic information depends on the purpose of the measurement and the structure of the network. The relationship between Template and Data is shown in **Fig. 2**.

The Template shown on the left side of the upper block defines the fields of the Data shown on the right side of the upper block by defining the ID and length of Information Elements (IEs). Any flow Data Record, which is a unit of flow information, can be defined as a combination of IEs. For example, an

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Table 1. Summary of protocols for measuring network traffic.

Protocol	SNMP	sFlow	NetFlow (v9)	IPFIX
Type of information	MIB counter	Partial packets chosen by sampling	Flow	
Amount of data	Small	Large (depending on sampling rate)	Between SNMP and sFlow (depending on sampling rate and flow creation conditions)	
Collectable information	Amount of data of interface	Data from data-link layer (containing packet header and data of partial packet payload)	Data from data-link layer to transport layer	
				Data other than the above is collected by vendor extensions.
Status of standardization	RFC3411, RFC3418, etc. (standard)	RFC3176 (informational by InMon)	RFC3954 (informational by Cisco)	Stage immediately before publication as an RFC (standard)

MIB: management information base  
RFC: request for comments

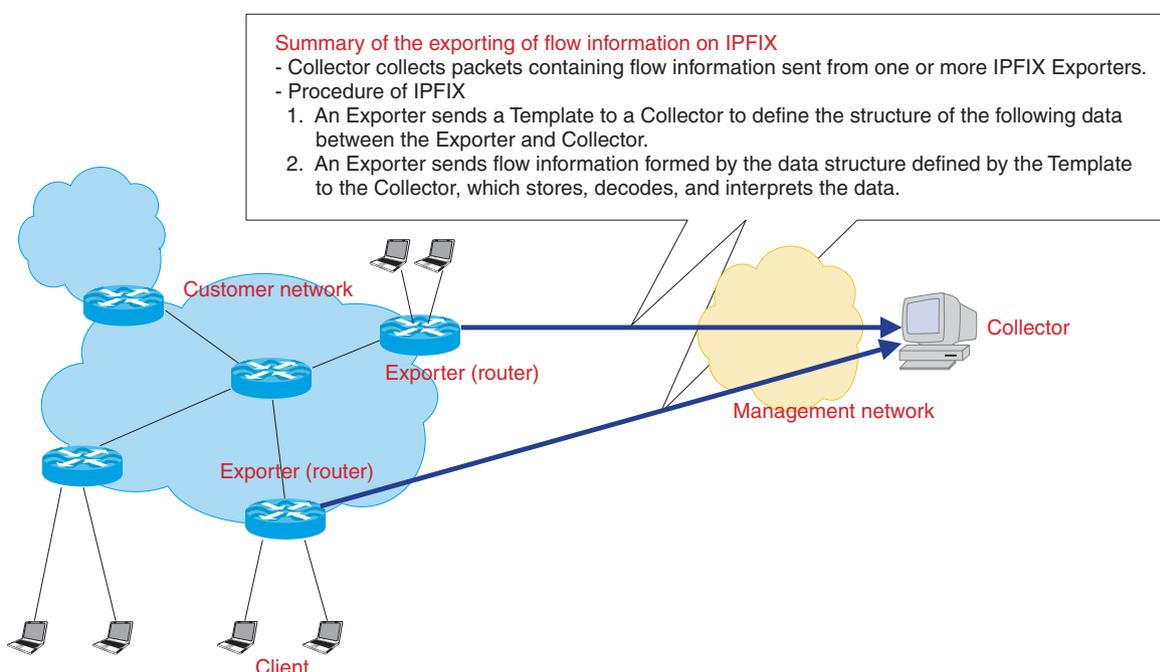


Fig. 1. Example of exporting flow information by IPFIX and NetFlow v9.

IPv6 (Internet protocol version 6) flow can be represented by using “sourceIPv6Address” instead of “sourceIPv4Address” and “destinationIPv6Address” instead of “destinationIPv4Address” in the Template shown in Fig. 2. Exporters send created Templates to Collectors by the following method.

The header of a Set (Set Header) is used to distinguish between Data and Templates. The Set ID contained in Set Header is 2 if the Set (which contains multiple Records) is a Template Set; it is 3 if the Set is an Option Template Set (described below), and it is a number between 256 and 65,535 if the Set is a Data Set.

A Template ID is used to relate a Template Record to a Data Record. The Template ID is contained in the Template Record Header if the Record is a Template Record. The Set ID is the same number as the Template ID (256 in Fig. 2) if the Record is a Data Record.

Option Templates and Data related to Option Templates provide optional information. An Option Template record is added to the Scope of a Template record to indicate the applicable scope of optional information. In the example shown in Fig. 2, the Scope is defined as a template ID in an Option Template Record, and the Option Data Record applies to





Photo 2. Gbit-RNP.

flow information and process it quickly. This difficulty is one of the main obstacles to the introduction of flow-based traffic measurement in advanced large networks like the NGN.

### 3.1 Proposals to IETF

The drafts “Reference Model for IPFIX Mediators” [3] and “Order of Information Elements” [4] have been proposed to the IPFIX working group (WG) of IETF by NTT NS Labs. and PF Labs., respectively, for the purpose of collecting flow information in a large-scale network.

#### 3.1.1 IPFIX Mediators

One application of IPFIX Mediators is an aggregator. This aggregates flows exported from the Exporter and exports aggregated data to Collectors in a cascade connection of IPFIX devices. Operators can obtain not only the rough trend of network traffic in a large-scale network, but also detailed flow data in a portion of the network because the IPFIX Mediators store the original exported flow data before aggregation.

#### 3.1.2 Order of IEs

This was proposed to achieve Collector implementation in a hardware-based fast collecting process with analyzing functions. IPFIX can configure exporting flow information by using the Template mechanism. Moreover, the Template mechanism allows IEs to be positioned regardless of data boundaries. Different orders of IEs among multiple Templates create different Templates with different formats even if the Templates contain the same set of IEs. Collectors must manage these templates individually even though their information is essentially the same. This redundancy is an inefficient implementation of the Collector in hardware, which has resource constraints. The proposal reduces the occurrences of inefficient situations. Even if the order is unified, the features of IPFIX will not be affected. In the draft, the order is considered based on the sizes of

IEs.

Hardware designed based on the dataflow architecture is suitable for processing information that is ordered. Although the processing of this architecture depends on the order of incoming data, the architecture can process data in parallel using many small and simple processing units. An overview of the dataflow architecture is given in **Fig. 3**. This architecture can achieve a higher degree of parallel processing than a general CPU (central processing unit) architecture for an general-purpose personal computer, so the dataflow architecture can achieve higher processing performance than a general CPU running at the same clock frequency. MFWv4 with Gbit-RNP, which can process flow information at a wire speed of 1 Gbit/s, uses this architecture. We expect to achieve higher performance by introducing a unified order and using the dataflow architecture.

The unified order can yield higher performance with not only the hardware Collector using the dataflow architecture but also a software-based Collector running on an ordinary CPU. We implemented a primitive software-based Collector that copies data of predetermined fields in incoming data records into a file using the Collector’s internal data structure. This collector supports the copying of multiple items of IE data at once if these multiple IEs are positioned sequentially in a Template exported from the Exporter and in a Collector’s internal data structure. Our evaluation found that the speed of a primitive Collector’s processing, which stores data of predetermined fields in incoming data records using the same proposed order for IEs as in the Template exported from the Exporter, was up to 80% faster [5] than when the order between a Template exported from the Exporter and the internal data structure stored in the Collector was different. The reason for the improvement is that the probability of using a multiple copy function was higher when the same order was used between an Exporter and a Collector.

## 4. Conclusion

We presented flow-based traffic measurement methods, especially IPFIX and NetFlow v9, and our activities concerning these protocols. We will work to propose our ideas to IETF, and we will also improve the feasibility of the hardware-based Collector to make a high-performance Collector that can measure network traffic in a large-scale network.

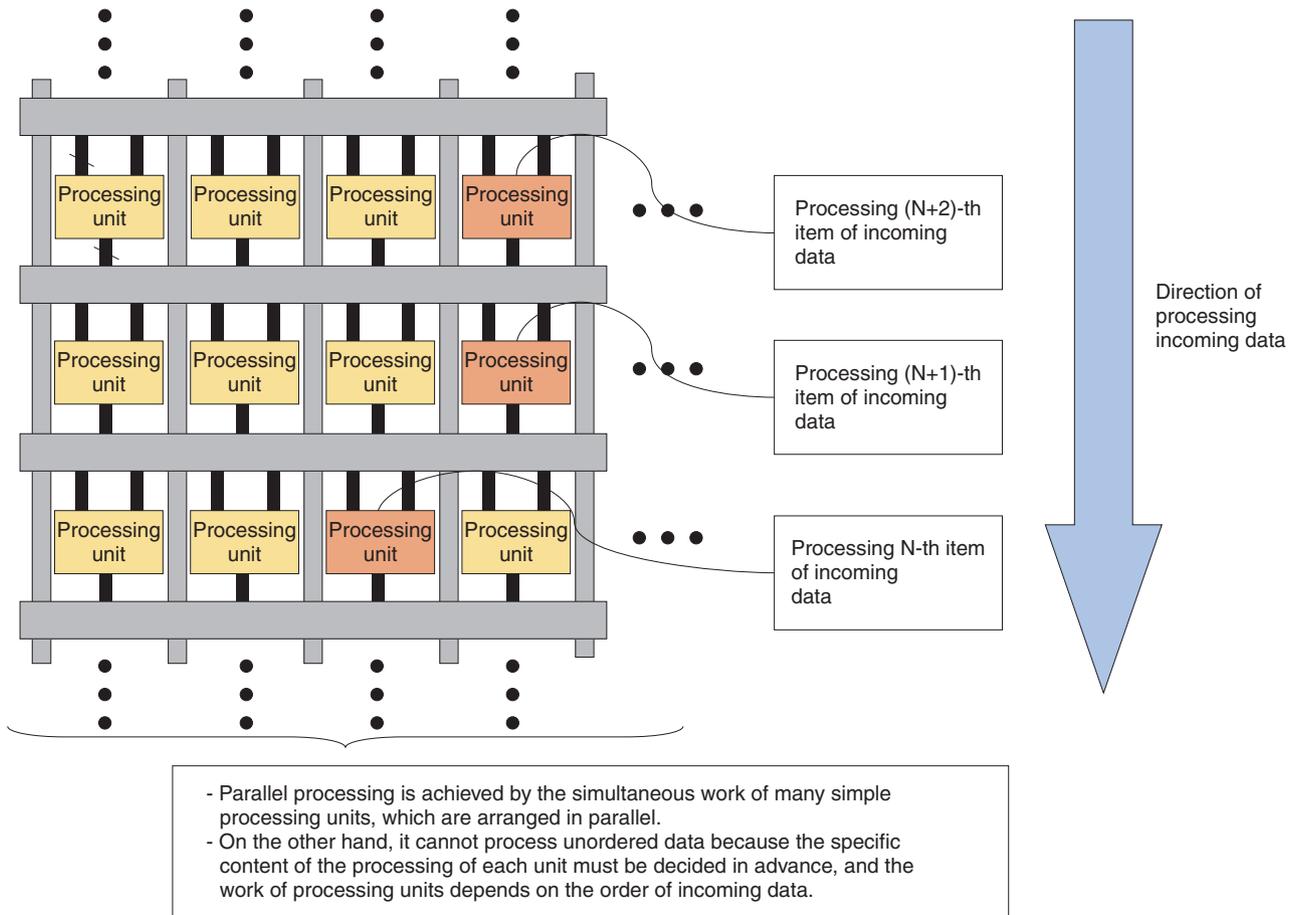


Fig. 3. Overview of the data flow architecture.

### References

[1] <http://www.ietf.org/html.charters/ipfix-charter.html>  
 [2] B. Claise, "Specification of the IPFIX Protocol for the Exchange of IP Traffic Flow Information," Internet Draft, draft-ietf-ipfix-protocol-26.txt, Sep. 2007 (work in progress).

[3] A. Kobayashi, K. Ishibashi, T. Kondoh, and D. Matsubara, "Reference Model for IPFIX Mediators," Internet Draft, draft-kobayashi-ipfix-mediator-model-01.txt, Nov. 2007 (work in progress).  
 [4] H. Irino, "Guidelines for the Order of Information Elements," draft-irino-ipfix-ie-order-03, Internet Draft, Nov. 2007 (work in progress).  
 [5] <http://www3.ietf.org/proceedings/07jul/slides/ipfix-10.pdf>

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